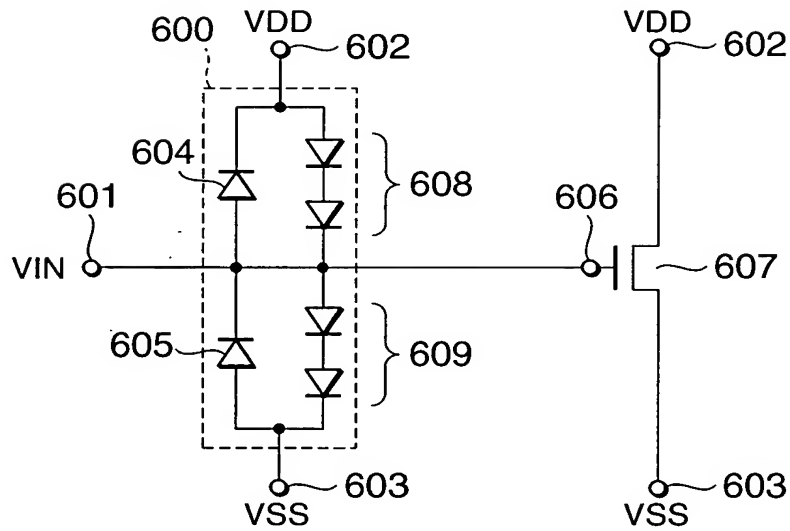


Fig. 1  
(PRIOR ART)



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Fig. 2A  
(PRIOR ART)

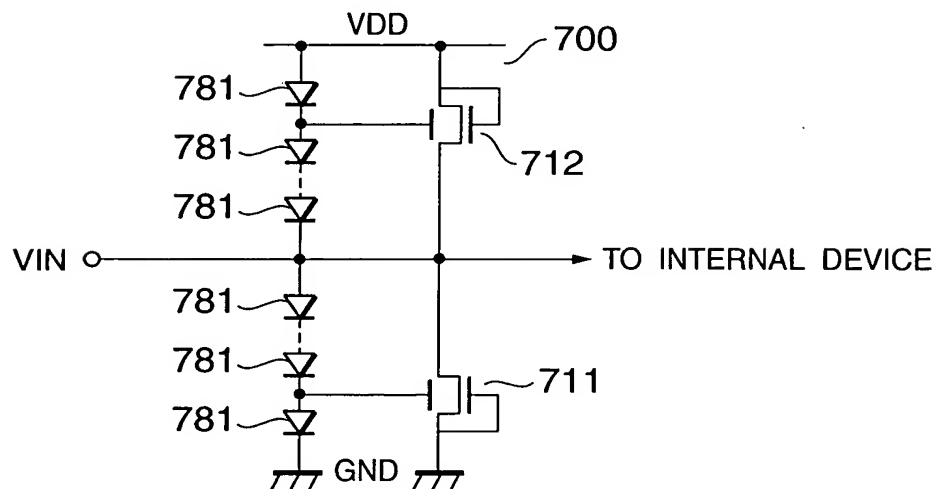
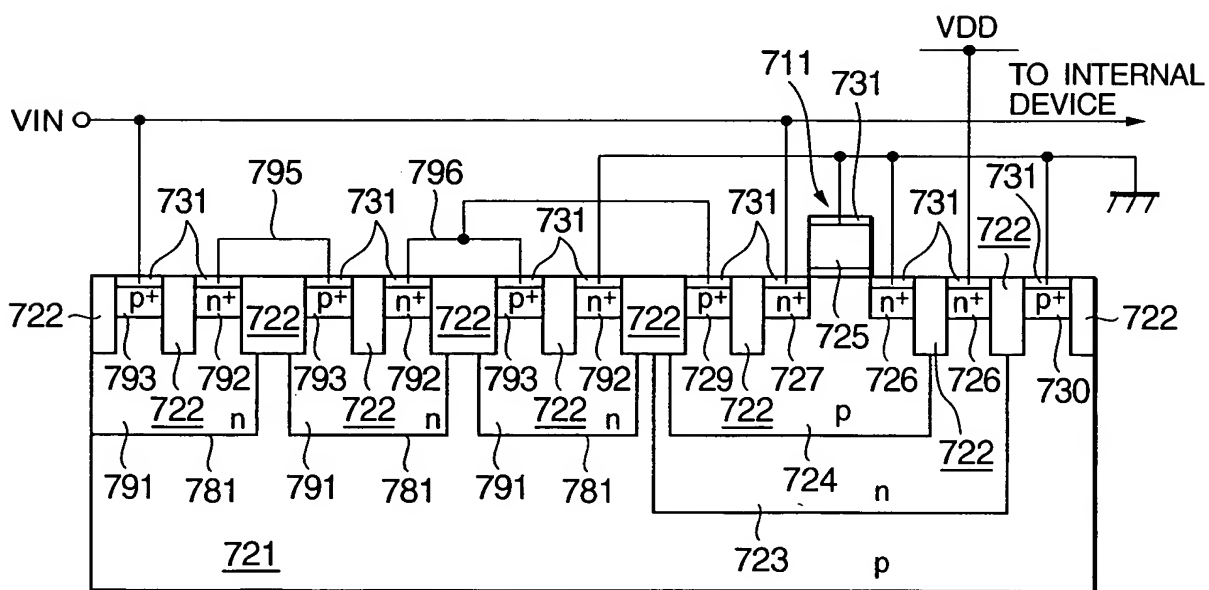
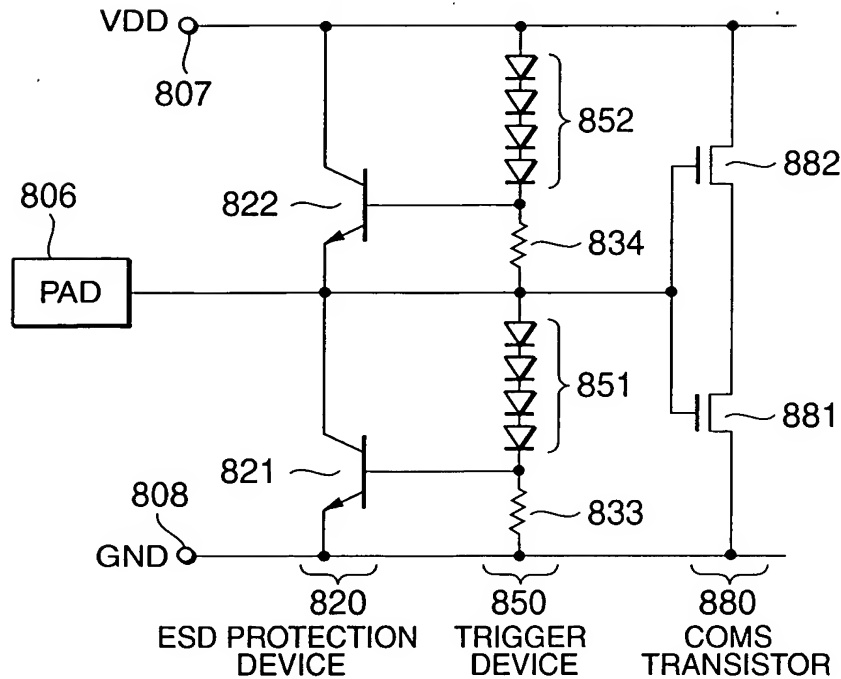


Fig. 2B  
(PRIOR ART)



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**Fig. 3A**  
 (PRIOR ART)



**Fig. 3B**  
 (PRIOR ART)

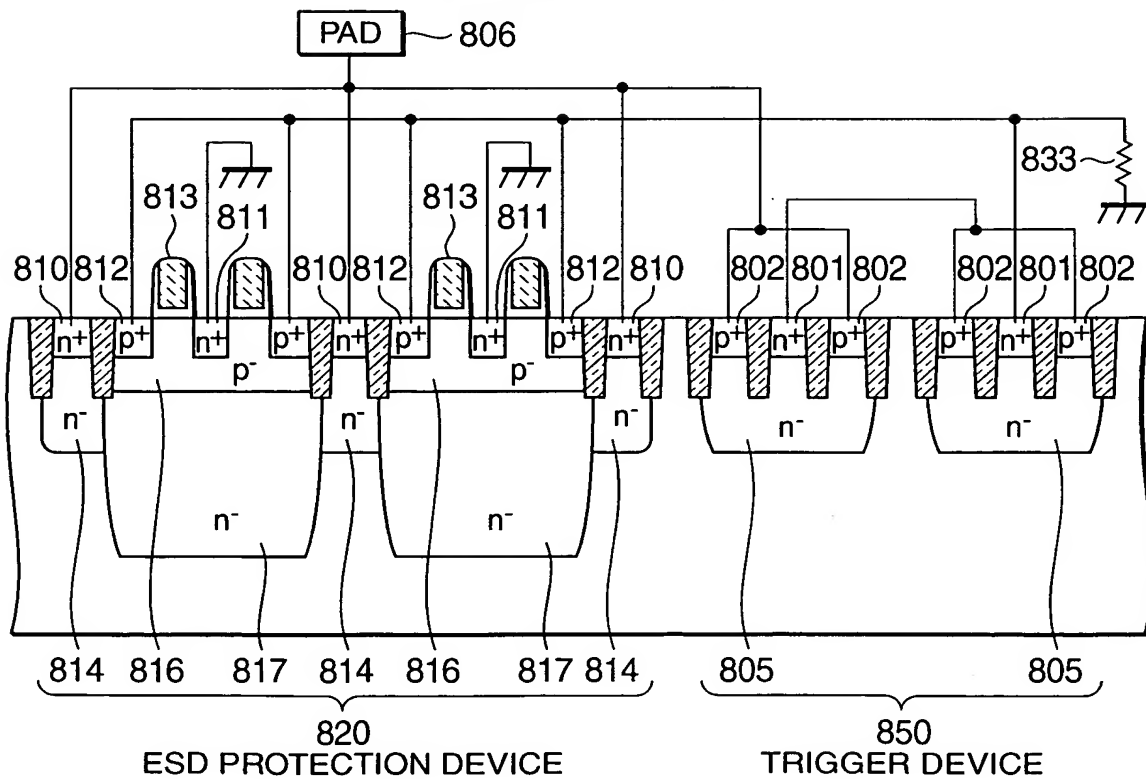
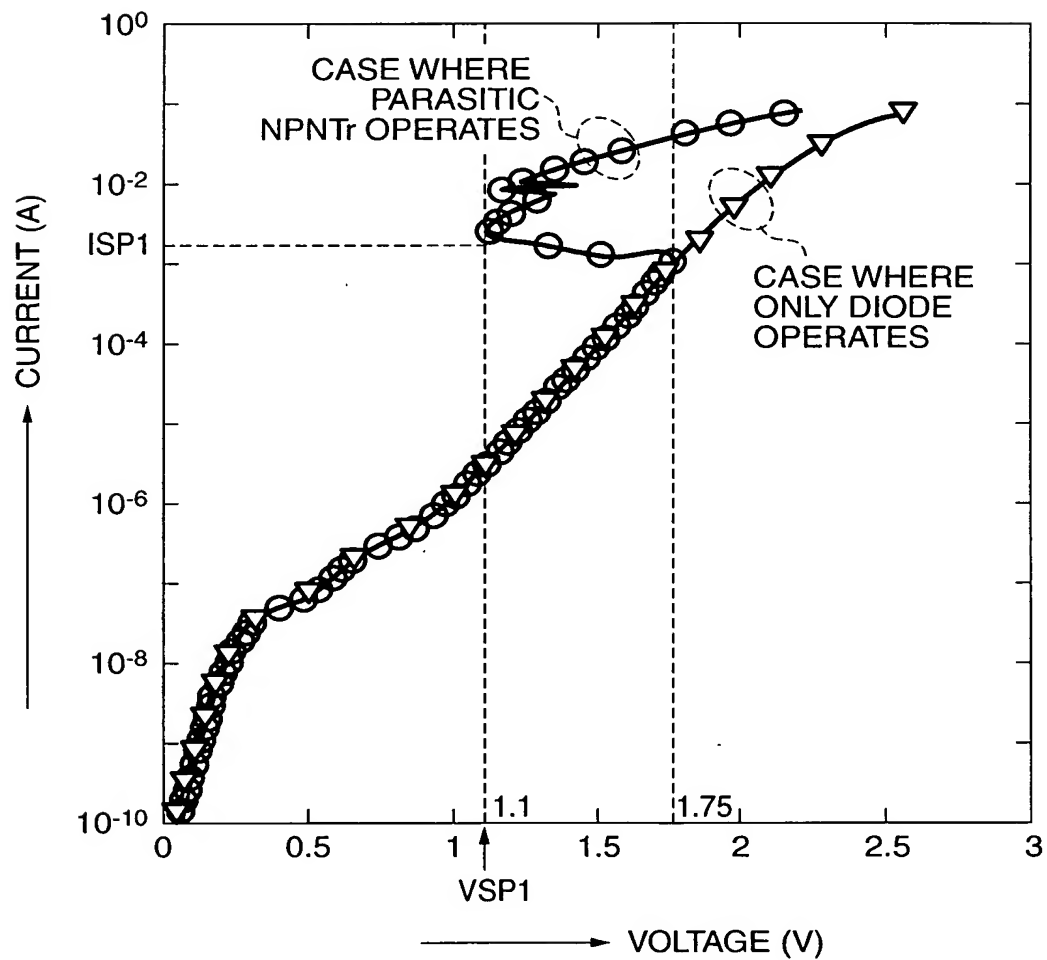




Fig. 5



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Fig. 6A

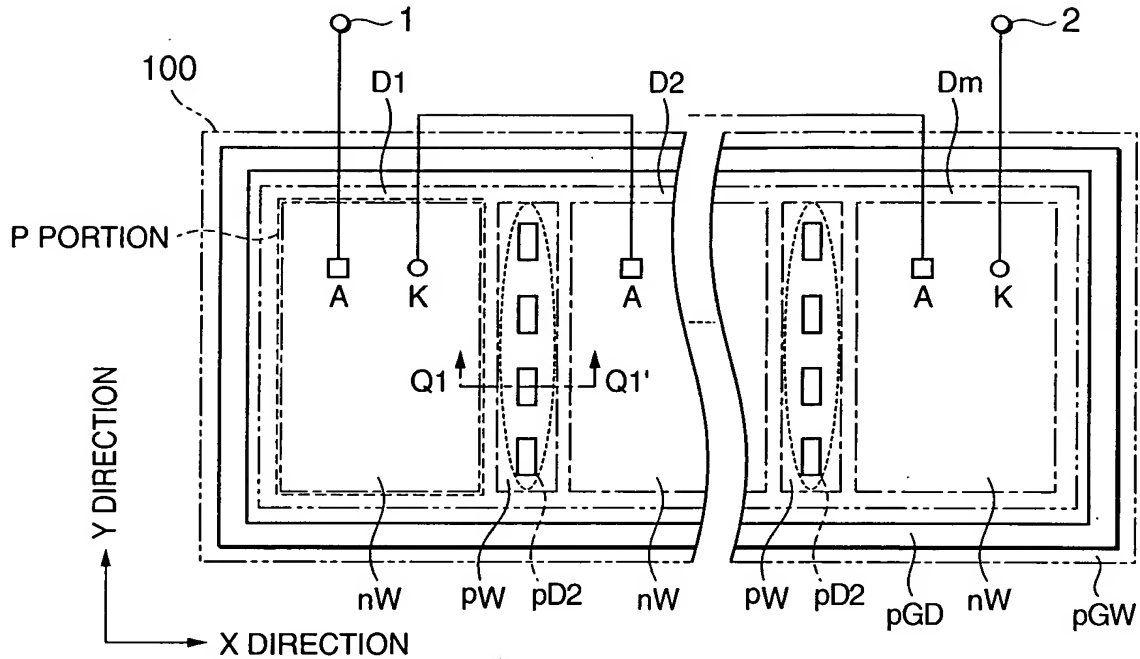


Fig. 6B

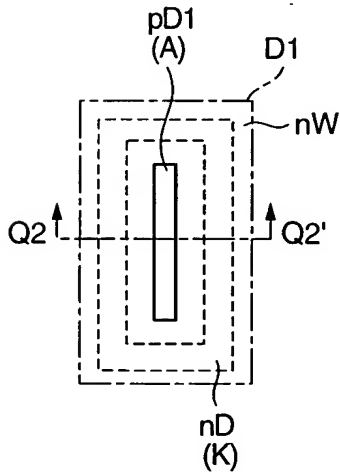


Fig. 6C

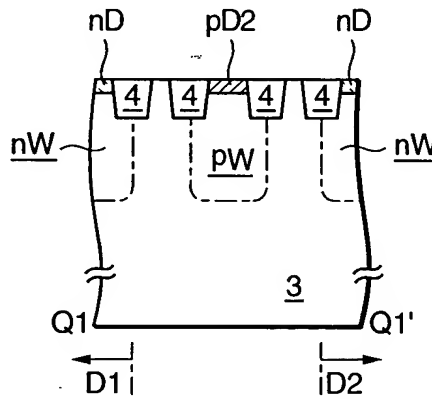


Fig. 6D

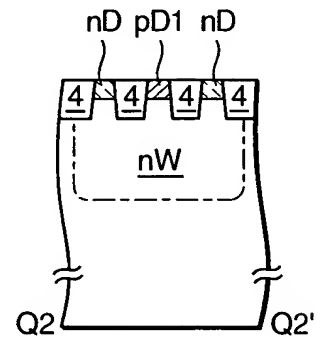
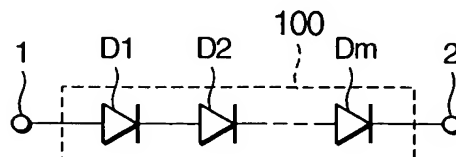


Fig. 6E



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Fig. 7A

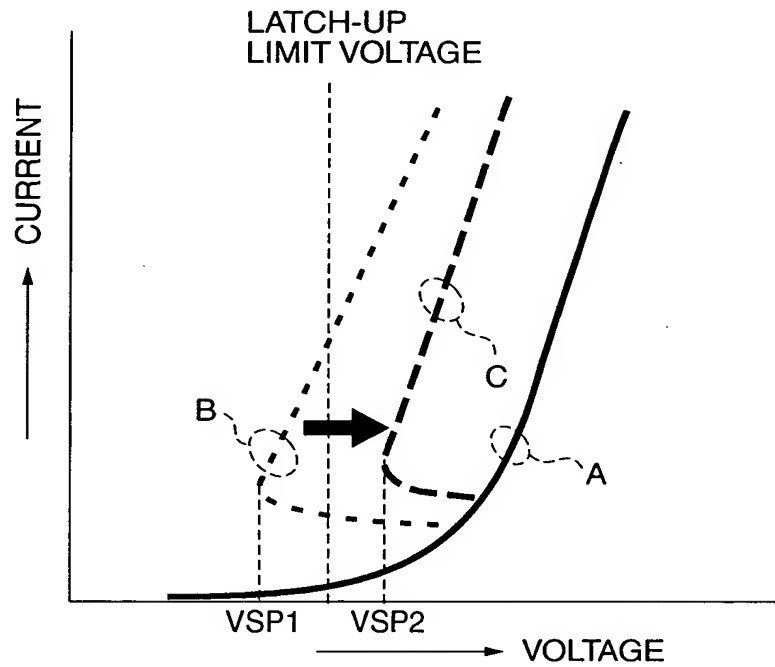
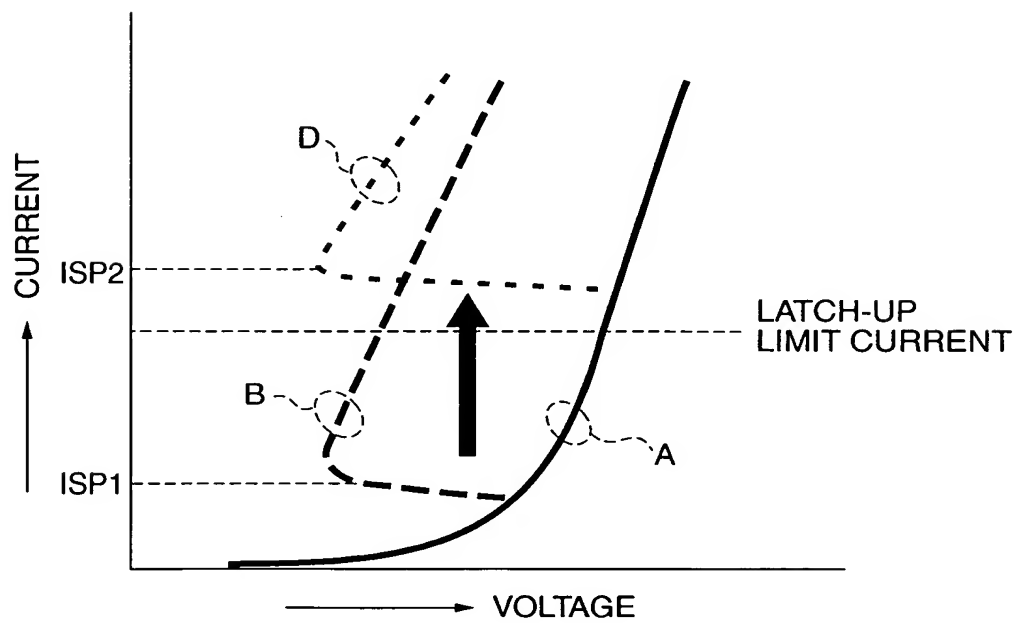


Fig. 7B



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Fig. 8A

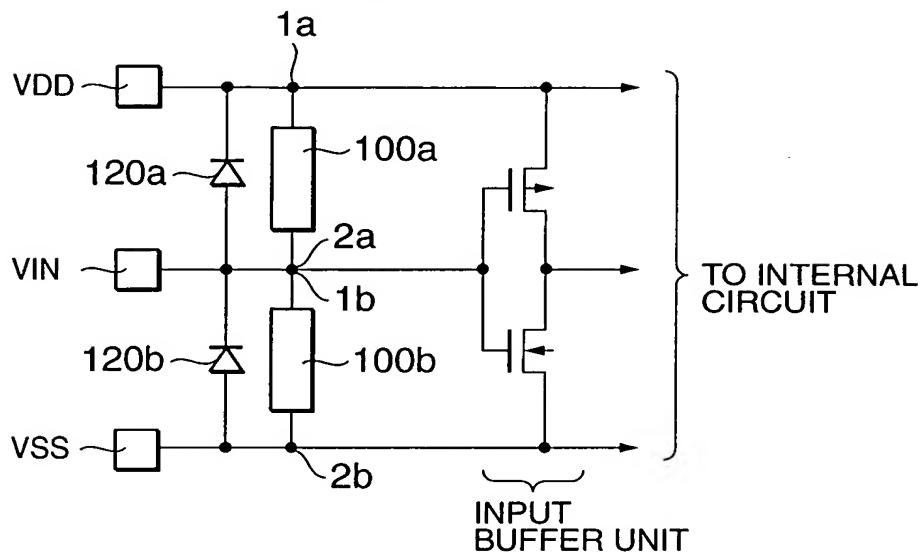


Fig. 8B

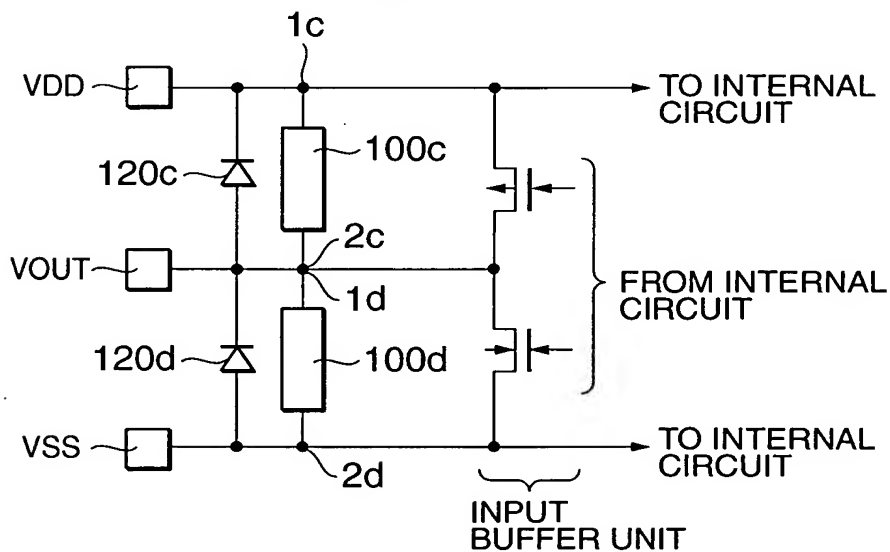
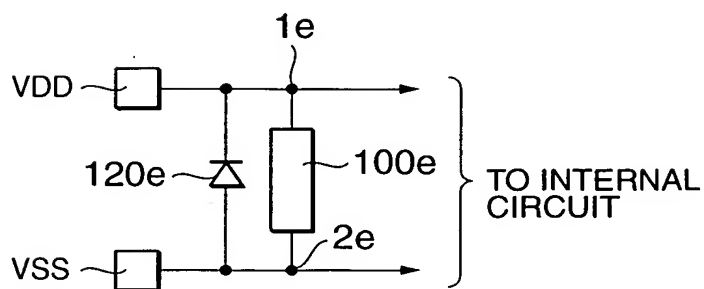


Fig. 8C





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Fig. 9A

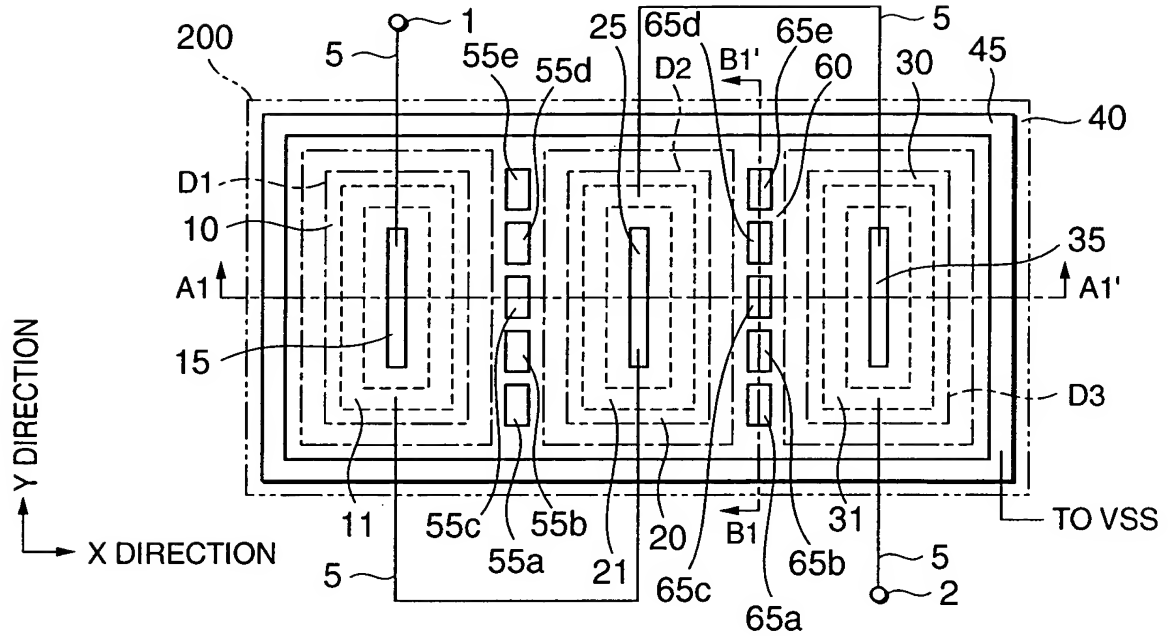


Fig. 9B

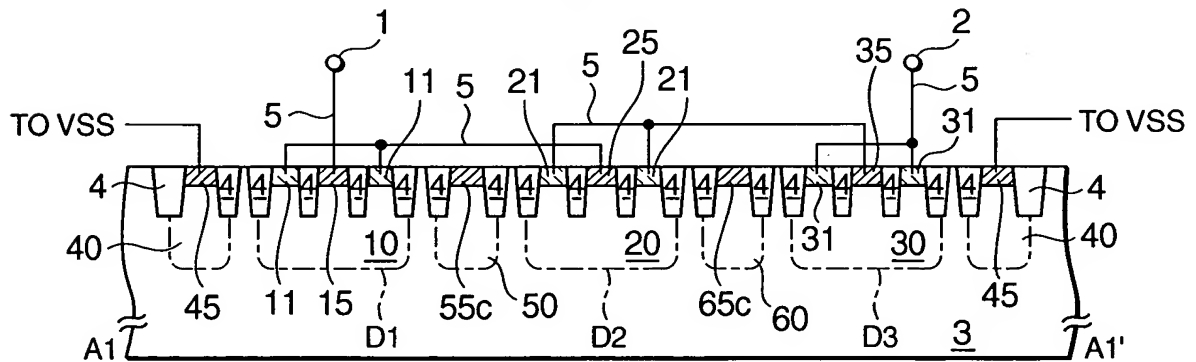
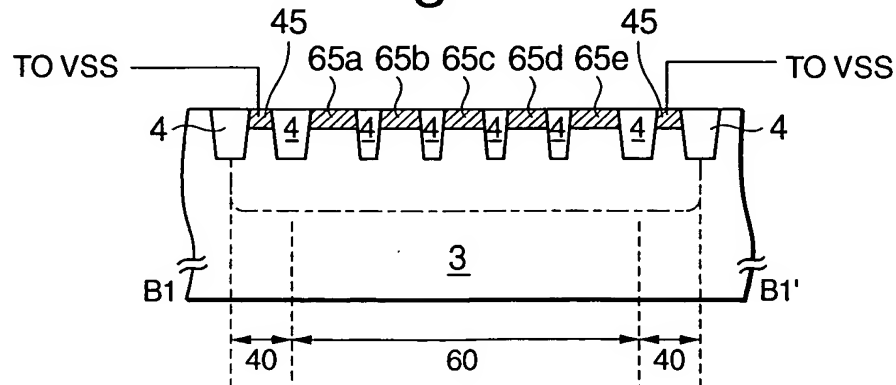


Fig. 9C



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Fig. 10A

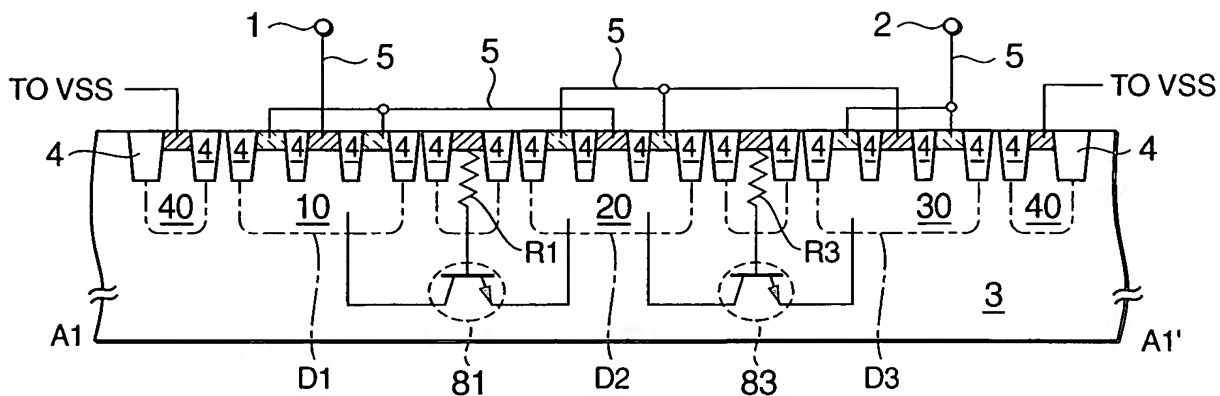


Fig. 10B

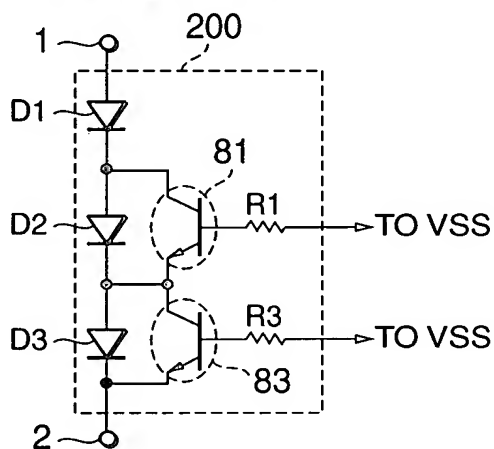


Fig. 10C

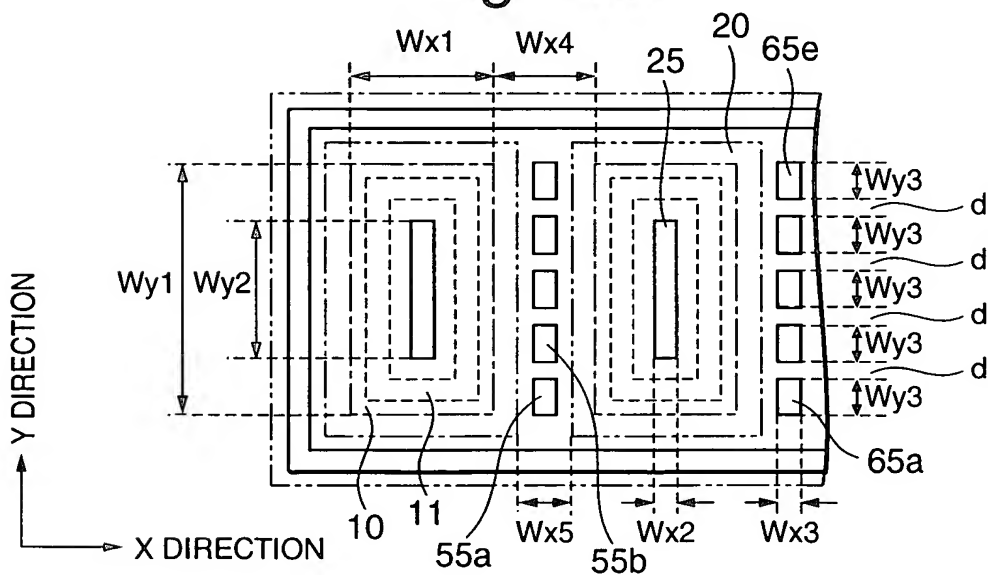


Fig. 11A

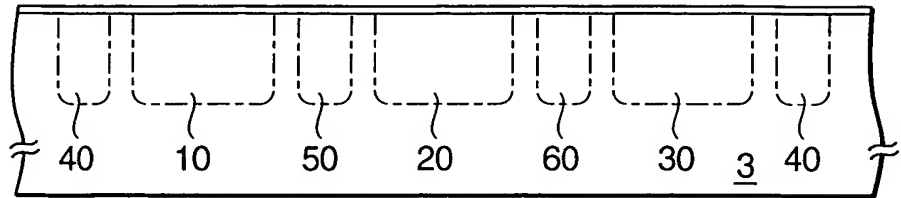


Fig. 11B

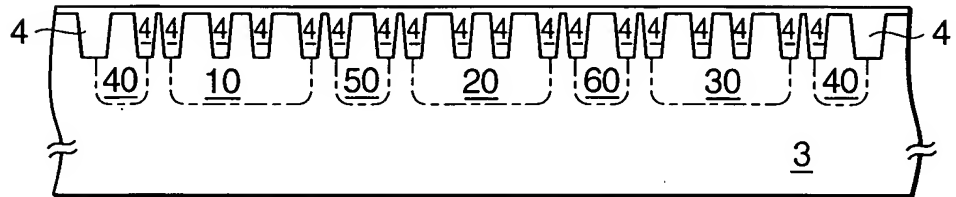


Fig. 11C

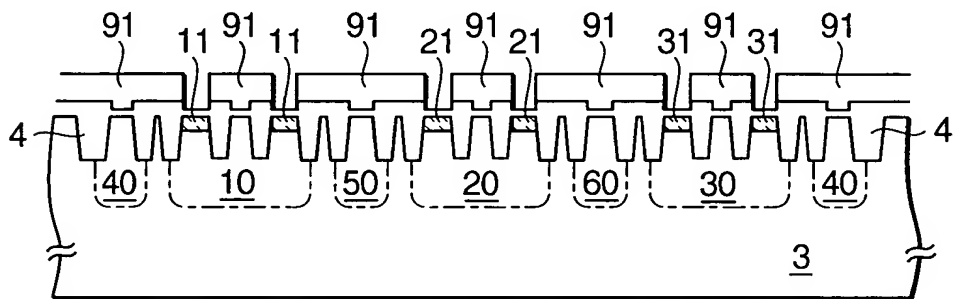


Fig. 11D

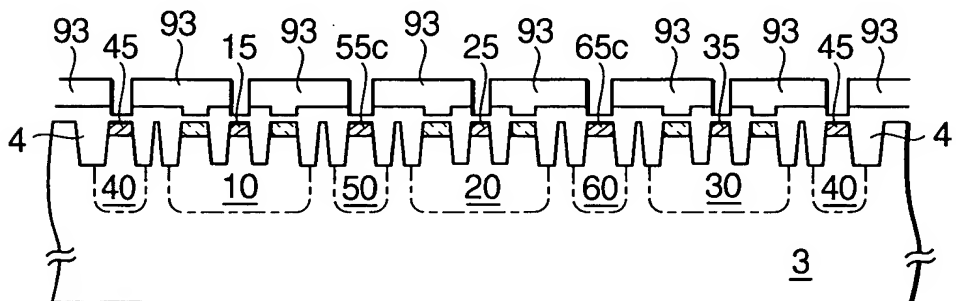
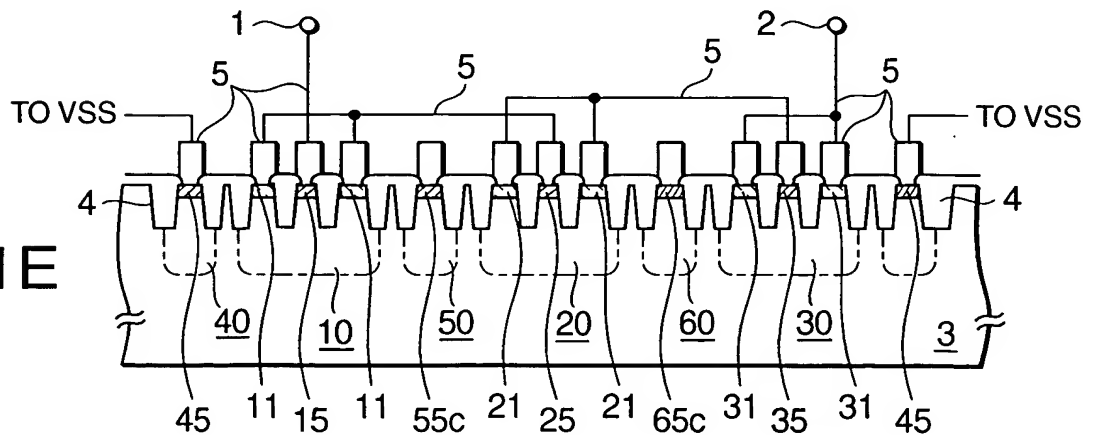


Fig. 11E





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Fig. 12A

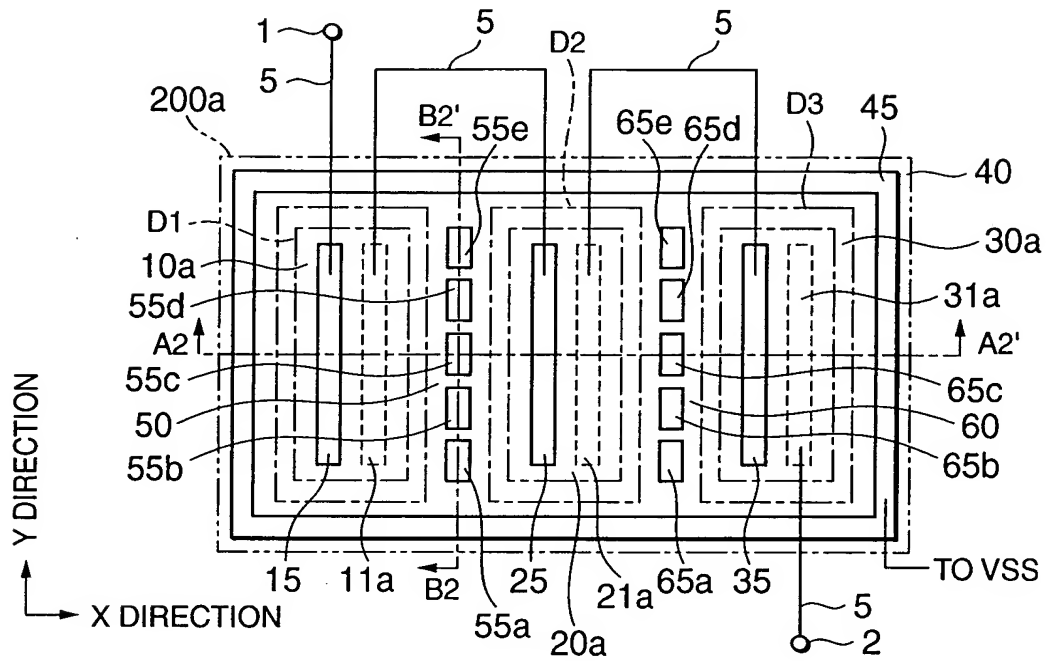


Fig. 12B

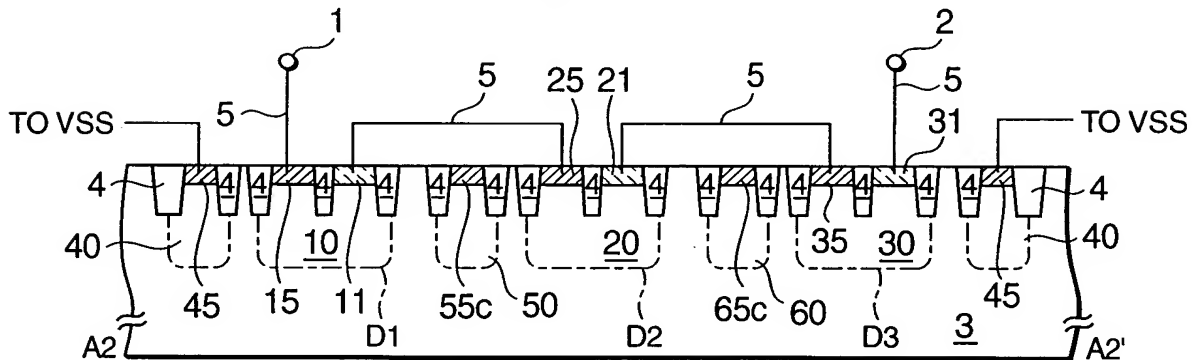
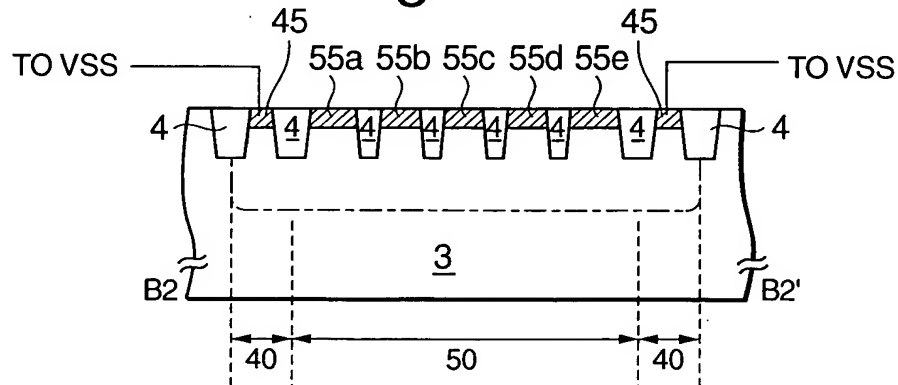


Fig. 12C



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Fig. 13A

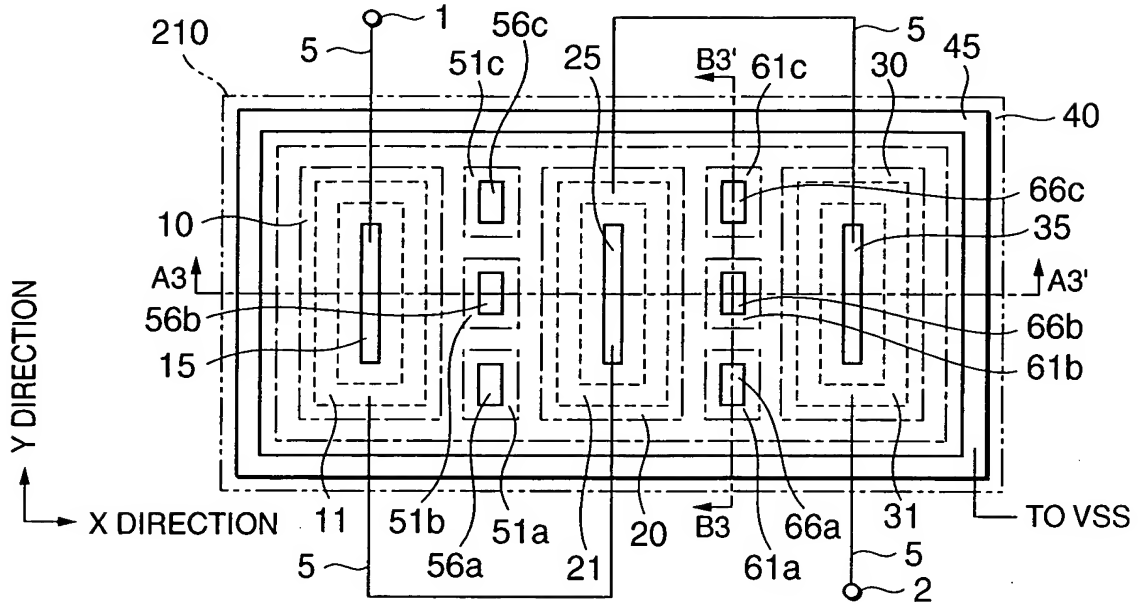


Fig. 13B

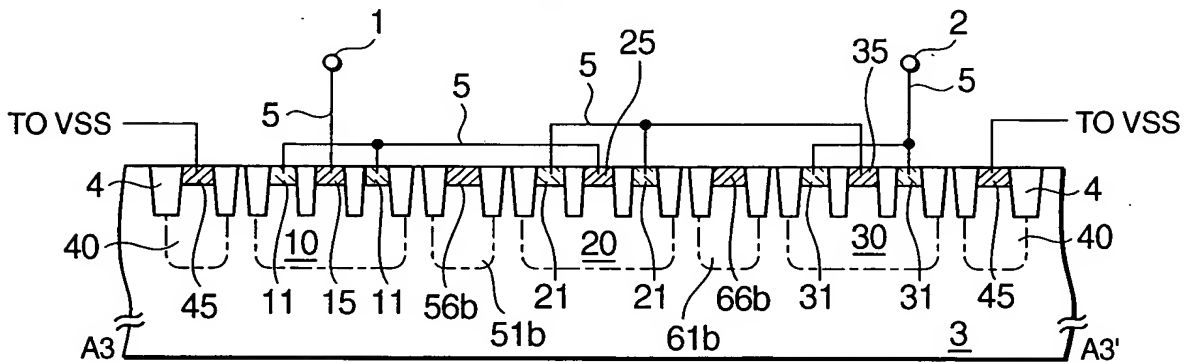
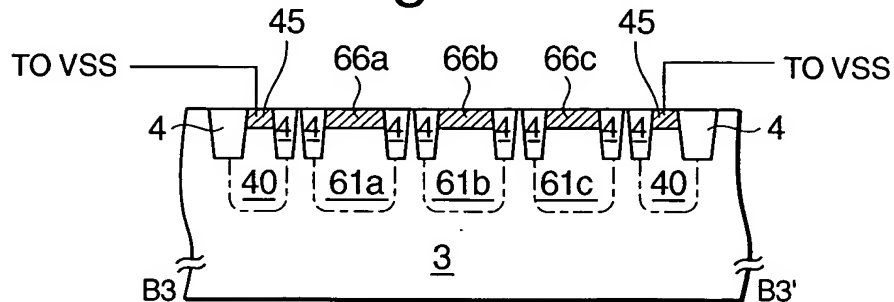


Fig. 13C



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Fig. 14A

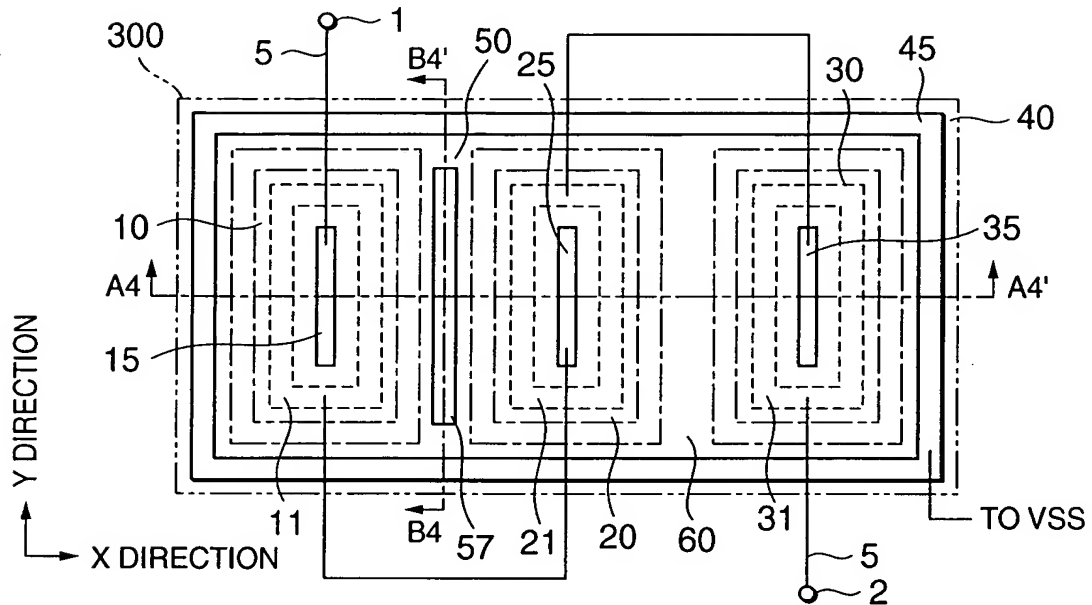


Fig. 14B

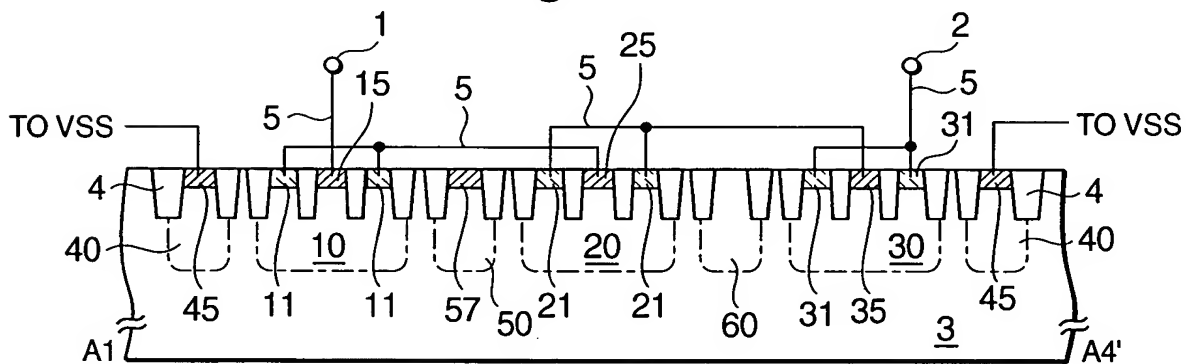
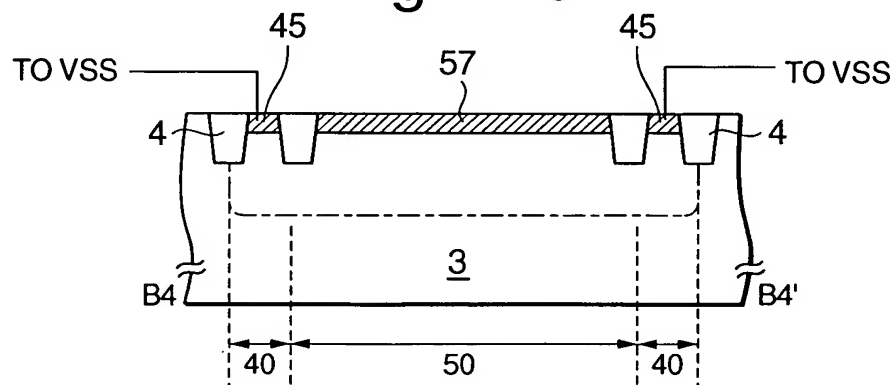


Fig. 14C



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Fig. 15A

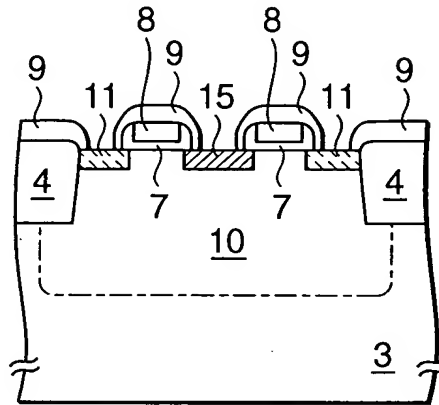


Fig. 15B

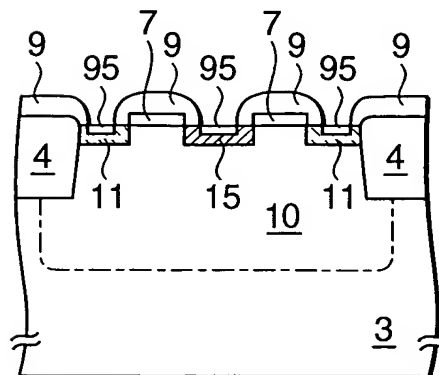




Fig. 16A

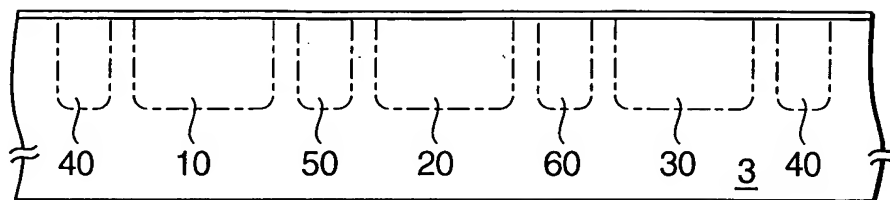


Fig. 16B

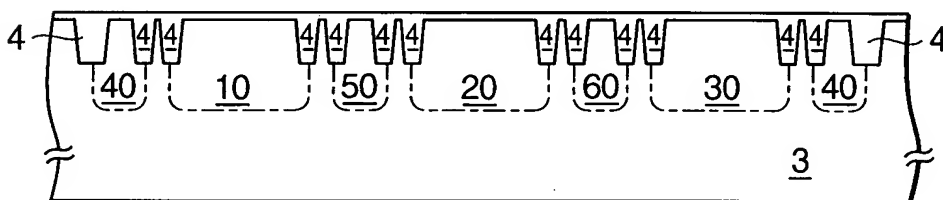


Fig. 16C

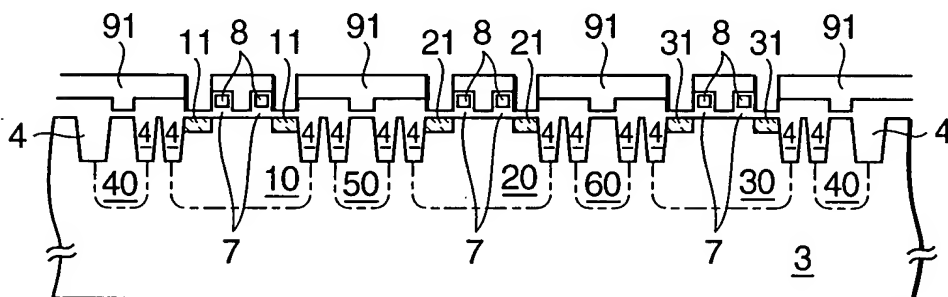


Fig. 16D

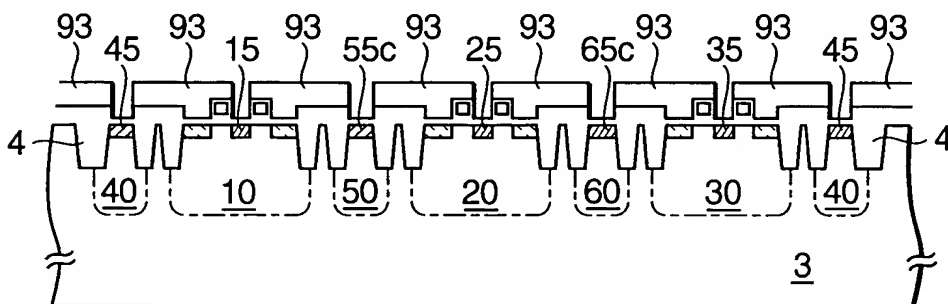


Fig. 16E

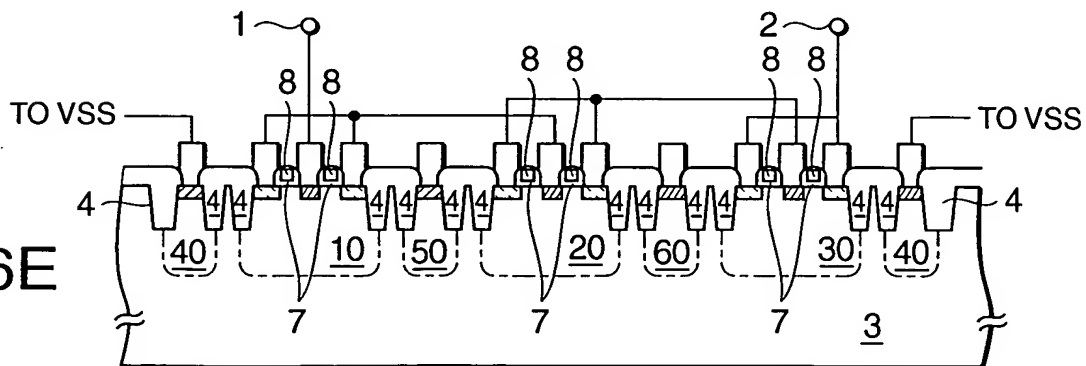


Figure 1 is a plan view of a semiconductor device. It shows a series of vertical strips (11, 15, 52, 57) and concentric rectangles (20, 21, 25) within a larger rectangular frame (40). The device is oriented with X and Y directions indicated. Various components are labeled with numbers: 1, 2, 5, 10, 11, 15, 20, 21, 25, 30, 31, 35, 40, 45, 50, 52, 57, 60, 67, 70, 75, 80, 85, 90, 95, 100, 105, 110, 115, 120, 125, 130, 135, 140, 145, 150, 155, 160, 165, 170, 175, 180, 185, 190, 195, 200, 205, 210, 215, 220, 225, 230, 235, 240, 245, 250, 255, 260, 265, 270, 275, 280, 285, 290, 295, 300, 305, 310, 315, 320, 325, 330, 335, 340, 345, 350, 355, 360, 365, 370, 375, 380, 385, 390, 395, 400, 405, 410, 415, 420, 425, 430, 435, 440, 445, 450, 455, 460, 465, 470, 475, 480, 485, 490, 495, 500, 505, 510, 515, 520, 525, 530, 535, 540, 545, 550, 555, 560, 565, 570, 575, 580, 585, 590, 595, 600, 605, 610, 615, 620, 625, 630, 635, 640, 645, 650, 655, 660, 665, 670, 675, 680, 685, 690, 695, 700, 705, 710, 715, 720, 725, 730, 735, 740, 745, 750, 755, 760, 765, 770, 775, 780, 785, 790, 795, 800, 805, 810, 815, 820, 825, 830, 835, 840, 845, 850, 855, 860, 865, 870, 875, 880, 885, 890, 895, 900, 905, 910, 915, 920, 925, 930, 935, 940, 945, 950, 955, 960, 965, 970, 975, 980, 985, 990, 995. The device is connected to a power supply (VSS) and a ground (GND) terminal.

Fig. 18A

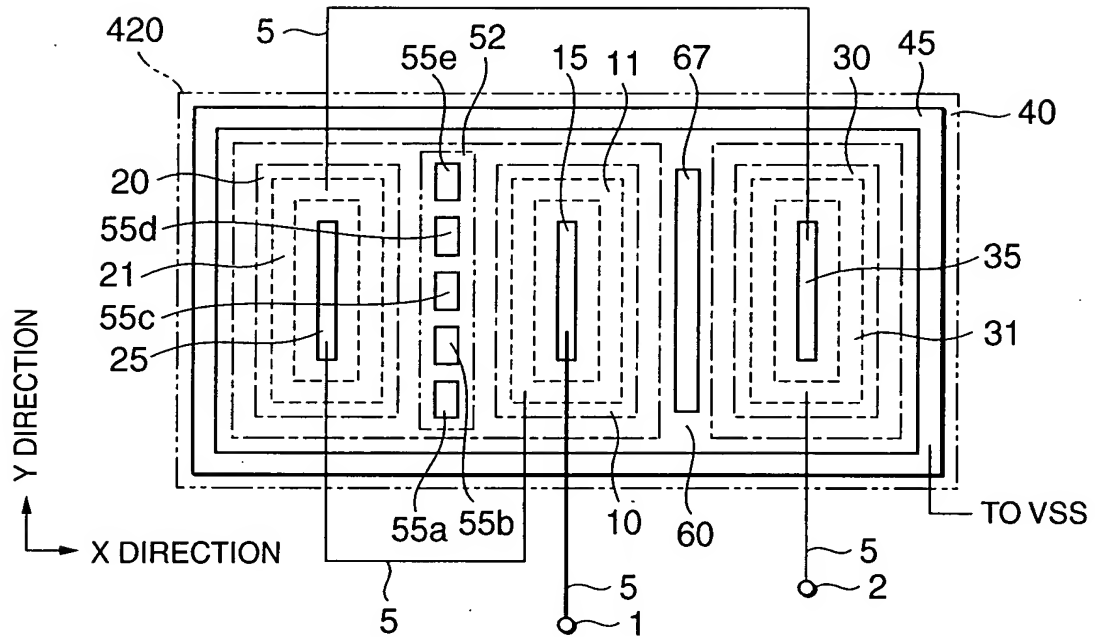


Fig. 18B

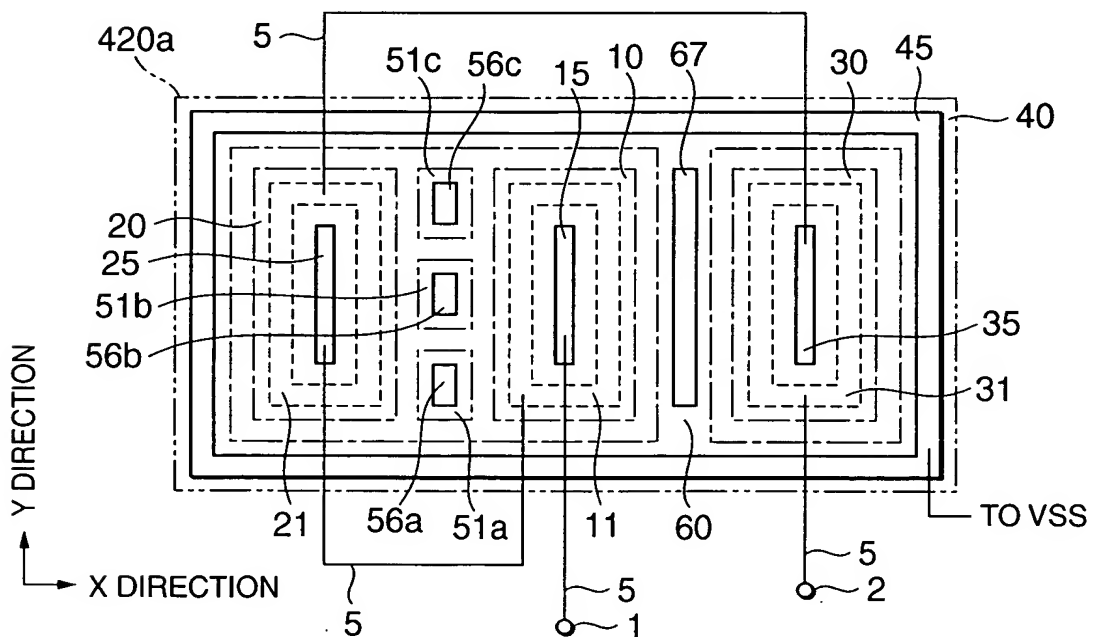
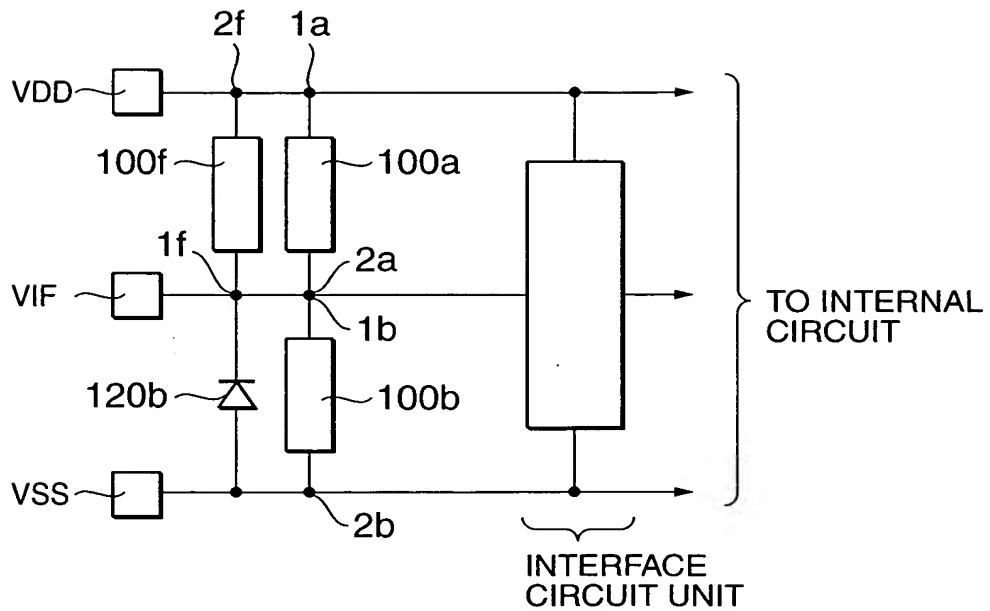


Fig. 19



INTERFACE CIRCUIT UNIT : INPUT BUFFER CIRCUIT,  
OUTPUT BUFFER CIRCUIT AND THE LIKE